SHT 1 COVER SHEET

SHT 2 MICROPROCESSOR, ROM, RAM, I/O

SHT 3 GATE ARRAY

SHT 4 MIDI, CLOCKS

SHT 5 ESP 1

SHT 6 ESP 2

SHT 7 ESP 3

SHT 8 ESP 4

SHT 9 KEYPAD/DISPLAY, FOOTSWITCH INPUTS

SHT 10 BYPASS CAPS, POWER SUPPLY CONN., UNUSED GATES

SHT 11 CV PEDAL A/D CONVERTER

- 5. INSTALL JUMPER R80, REMOVE JUMPER R81.
- 4. FOR ESP VOLTAGE OF 5.25V.

INSTALL: VR2, R74, R75, C115, C116 & C121.

REMOVE: VR3 & C114.

FOR ESP VOLTAGE OF 5.00V.

INSTALL: VR3, C114 & C116.

REMOVE: VR2, R74, R75, C115 & C121.

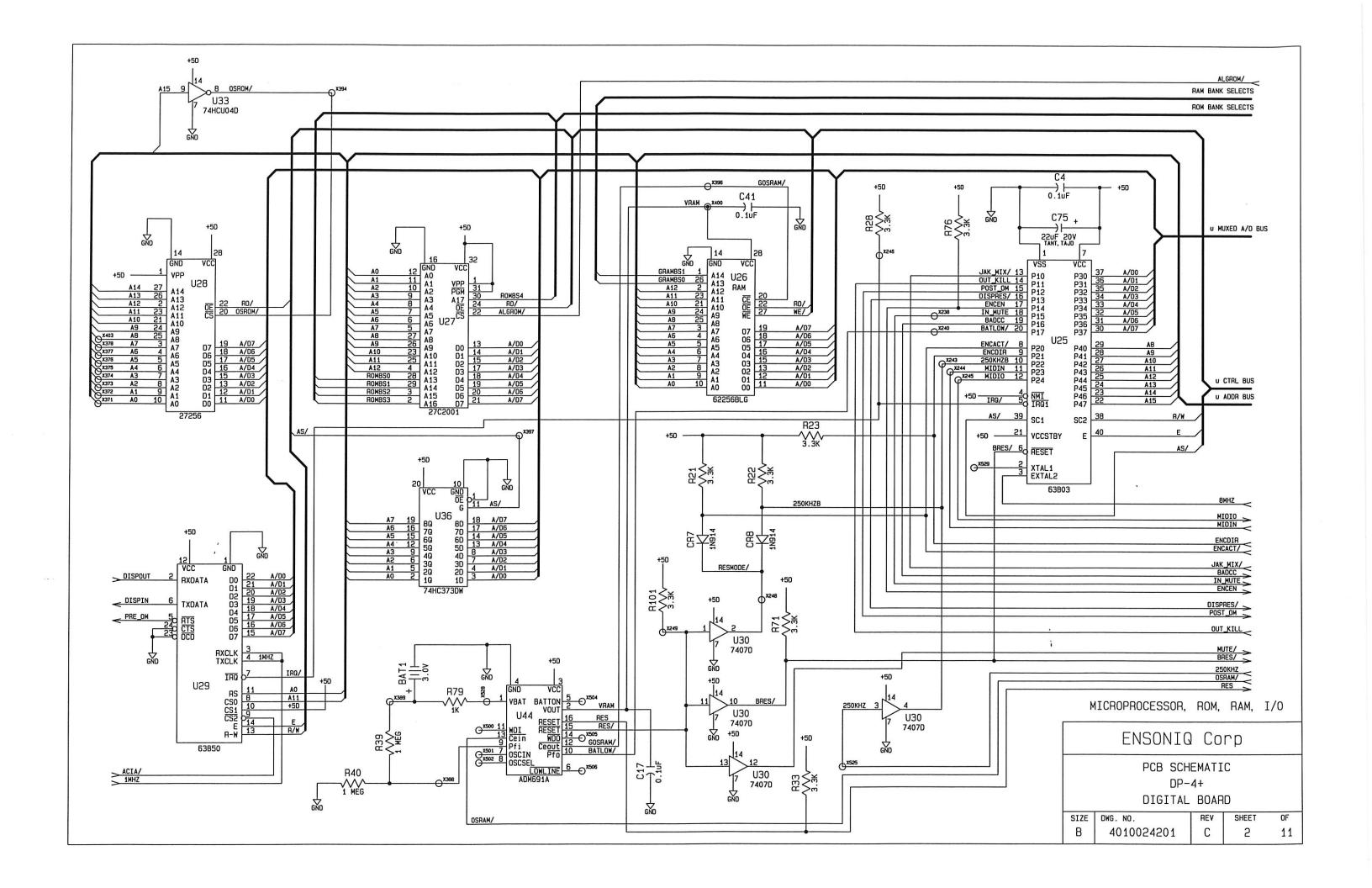
- 3. ALL 0.1UF BYPASS CAPACITORS ARE 25V +80/-20% Z5U.
- 2. ALL CAPACITOR VALUES ARE IN MICROFARADS, 16V MINIMUM.
- 1. ALL RESISTOR VALUES ARE IN OHMS, 1/8W 5%.

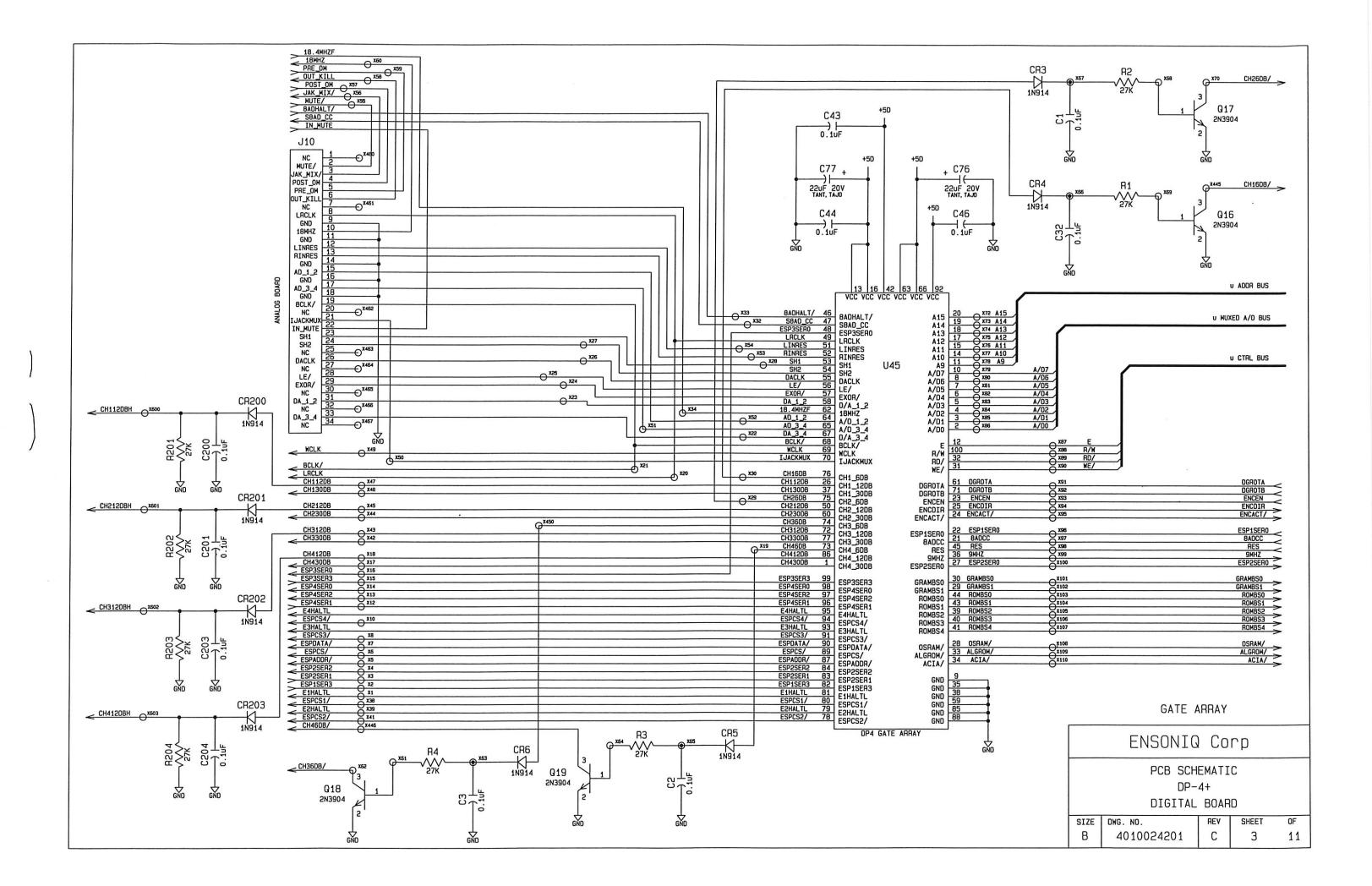
NOTES - UNLESS SPECIFIED OTHERWISE:

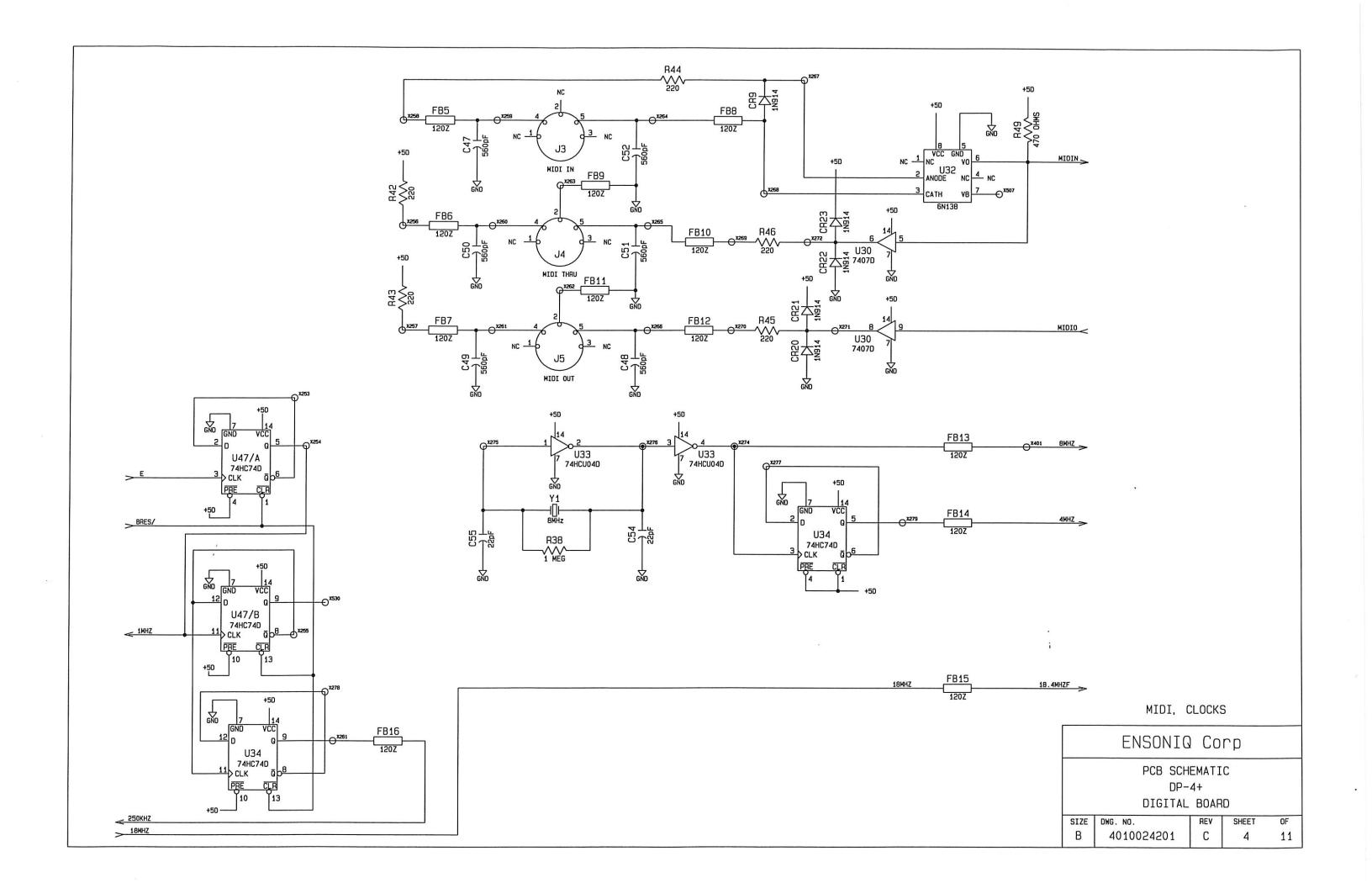
	REVISIONS			
REV	DESCRIPTION	DATE	APPROVED	
1	PROTOTYPE RELEASE			
2	PROTOTYPE RELEASE			
3	PROTOTYPE RELEASE			
4	PROTOTYPE RELEASE			
5	PROTOTYPE RELEASE		27	
6	PROTOTYPE RELEASE			
7	PROTOTYPE RELEASE			
Α	RELEASED PER EDR 42-528	12-21-94	AKS	
В	REVISED PER ECO 3057	2-4-95	AKS	
С	REVISED PER ECO 3070	3-70-05	FA	

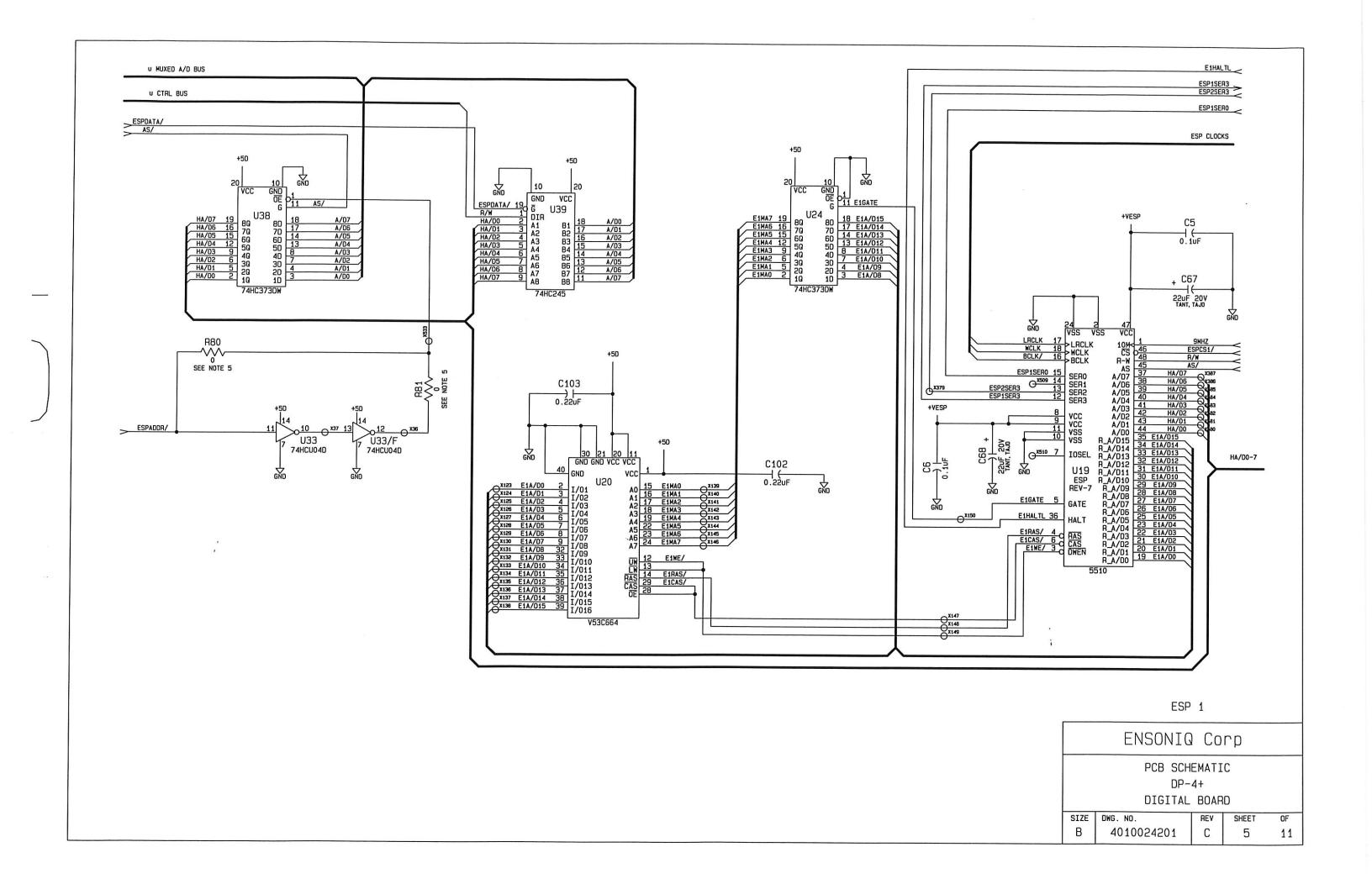
COVER SHEET

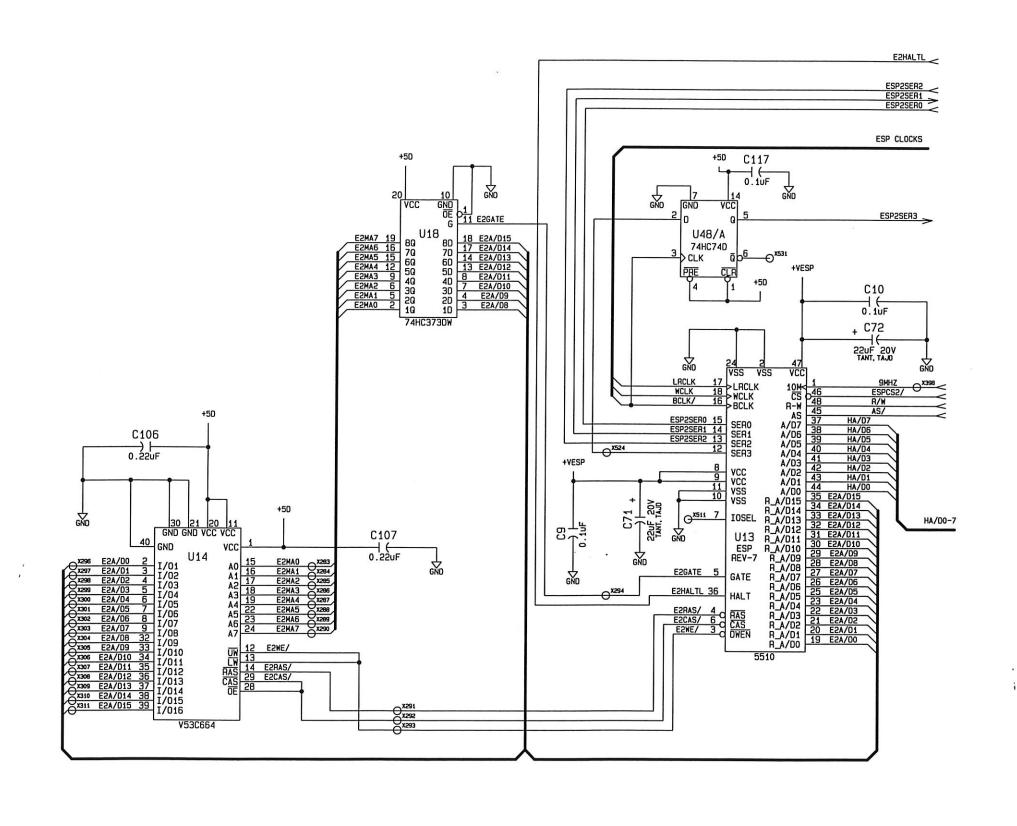
	ENSONIQ Corp							
1	155 GREAT VALLEY PKWY, MALVERN PA 19355							
APPROVALS DATE PCB SCHEMATIC								
DWN BY: AKS 02-18-94			DP-4+					
СНКО ВУ: DIGITAL BOARD								
APPD BY:			SIZE	DWG. NO.				REV
APPU BY:			В		401002	2420)1	С
USED ON:	DP-	-4 PRO	SCALE	_	SHEET	1	0F	11





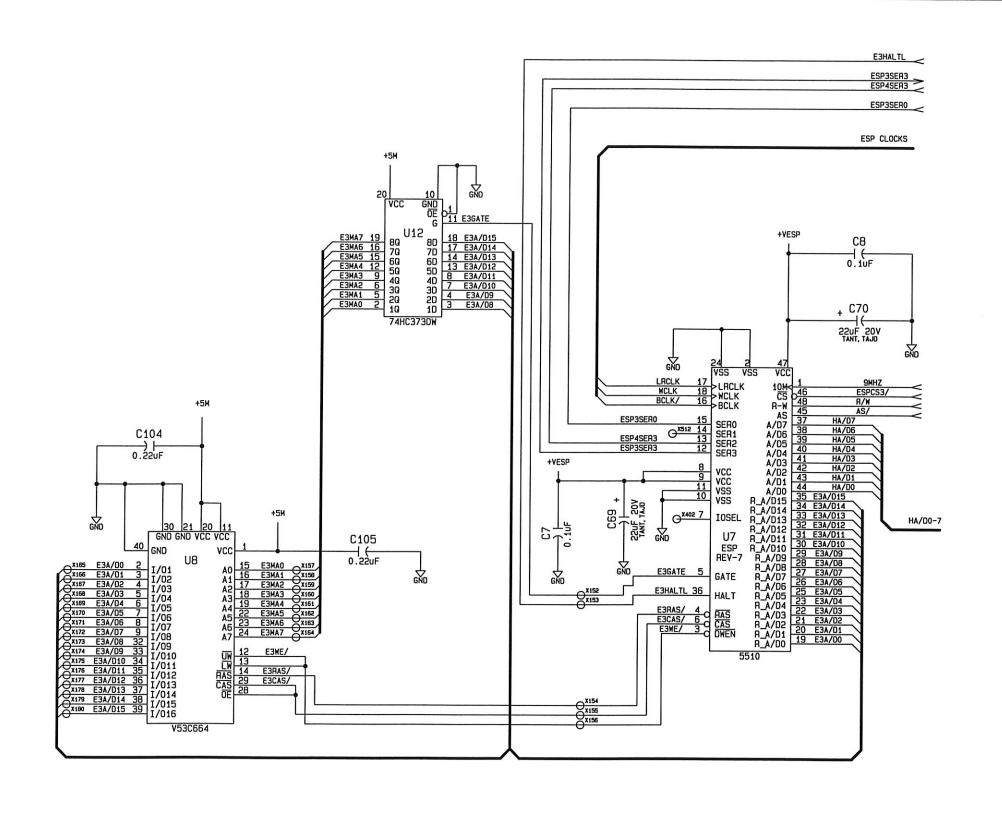






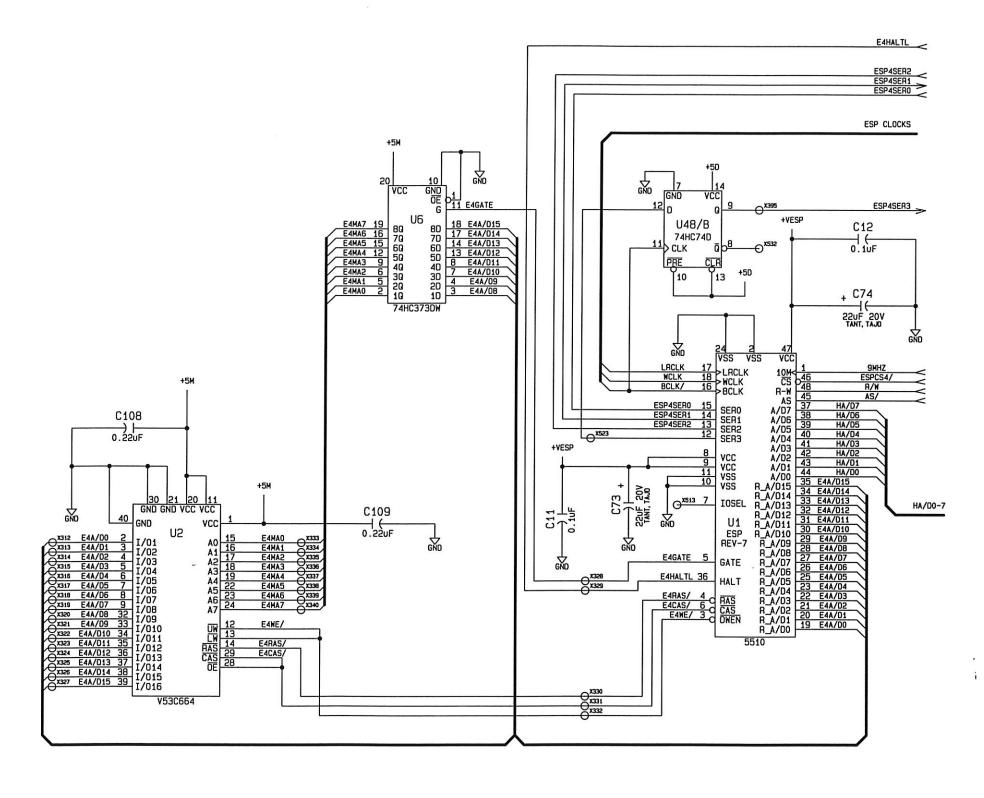
ESP 2

	ENSONIG	Co	rp	
PCB SCHEMATIC DP-4+ DIGITAL BOARD				
size B	DWG. NO. 4010024201	REV C	SHEET 6	0F 11



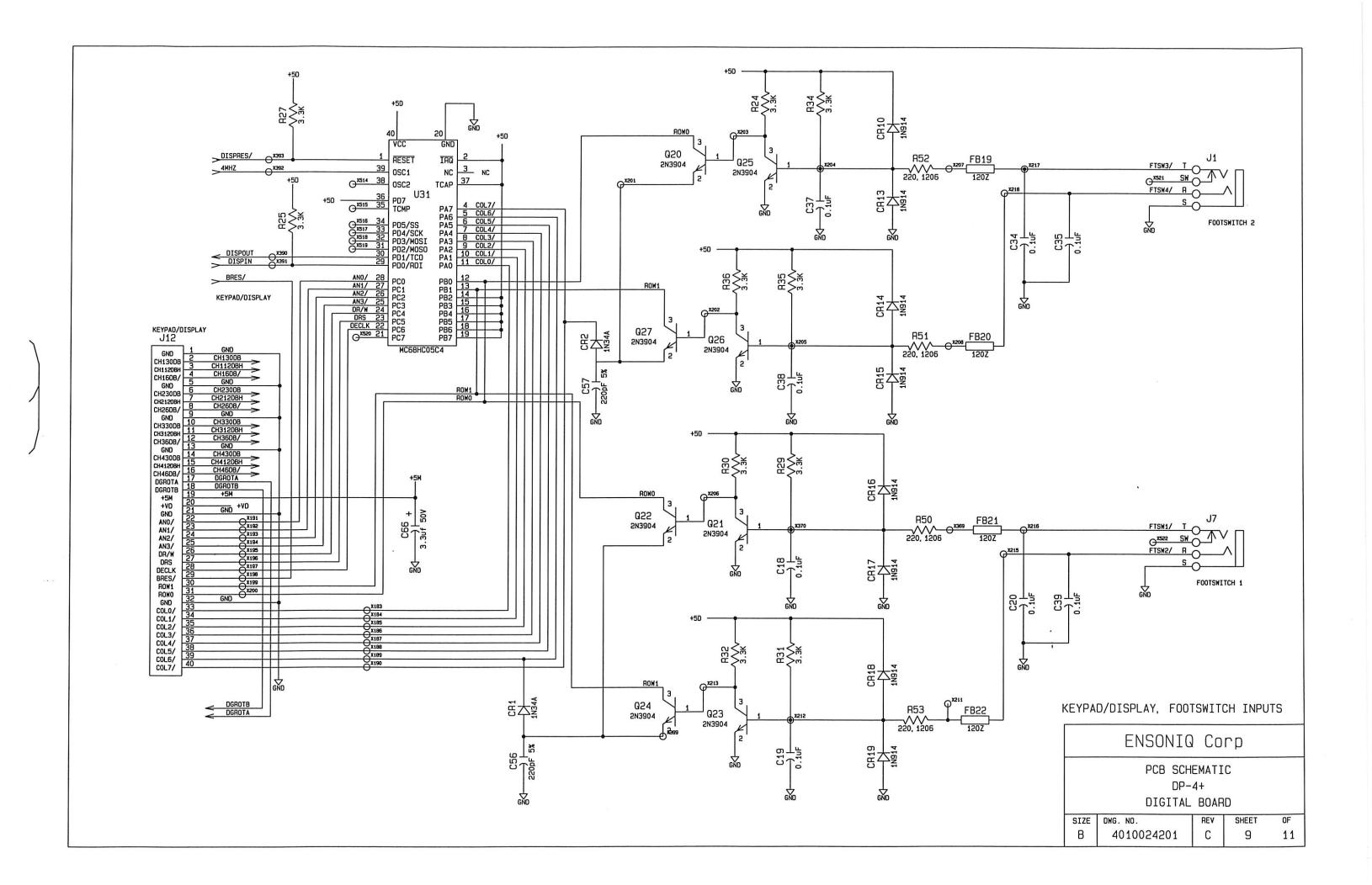
ESP 3

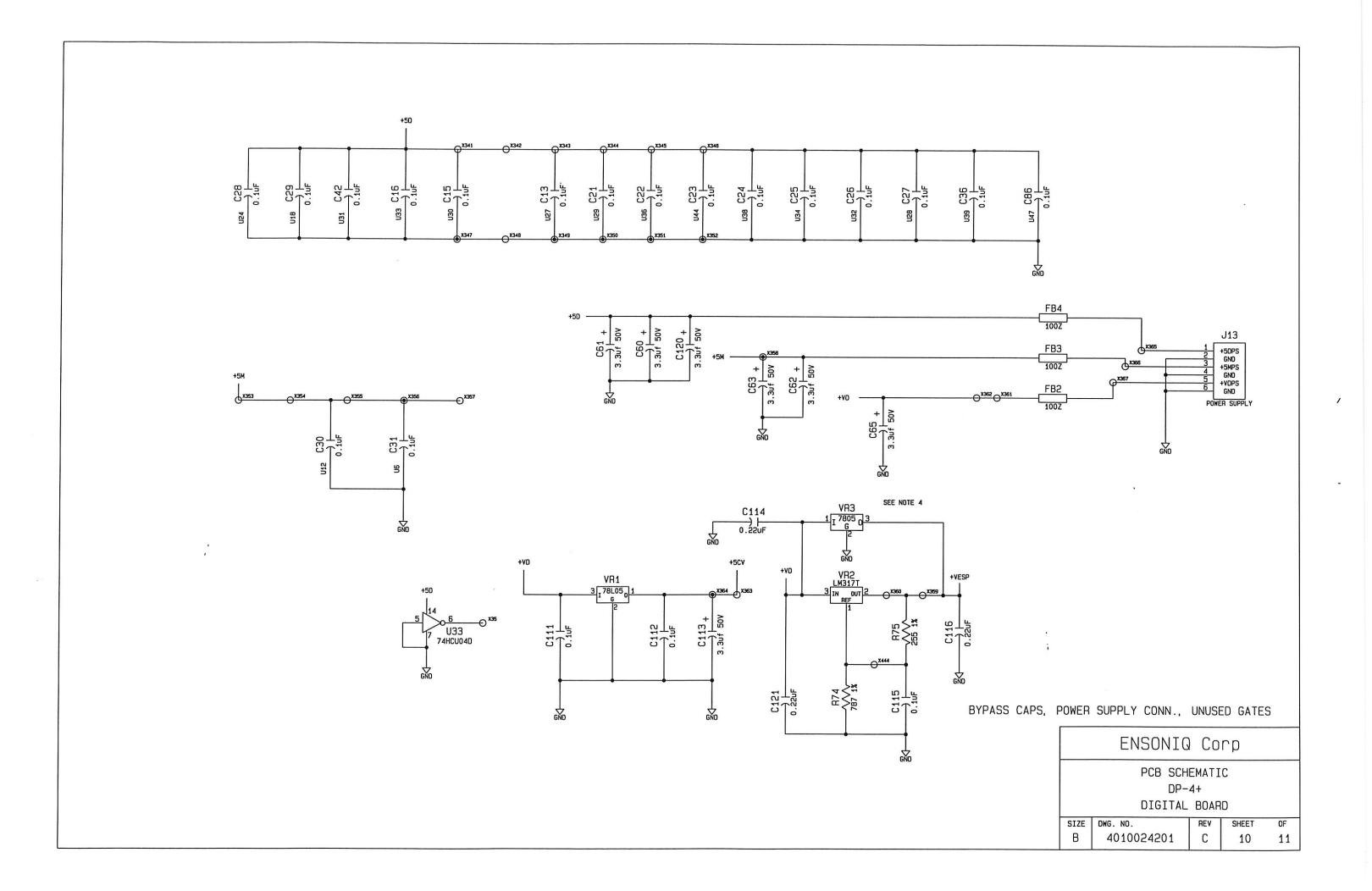
	ENSONIG	Co	rp		
PCB SCHEMATIC					
DP-4+					
DIGITAL BOARD					
SIZE	DWG. NO.	AEV	SHEET	0F	
В	4010024201	С	7	11	

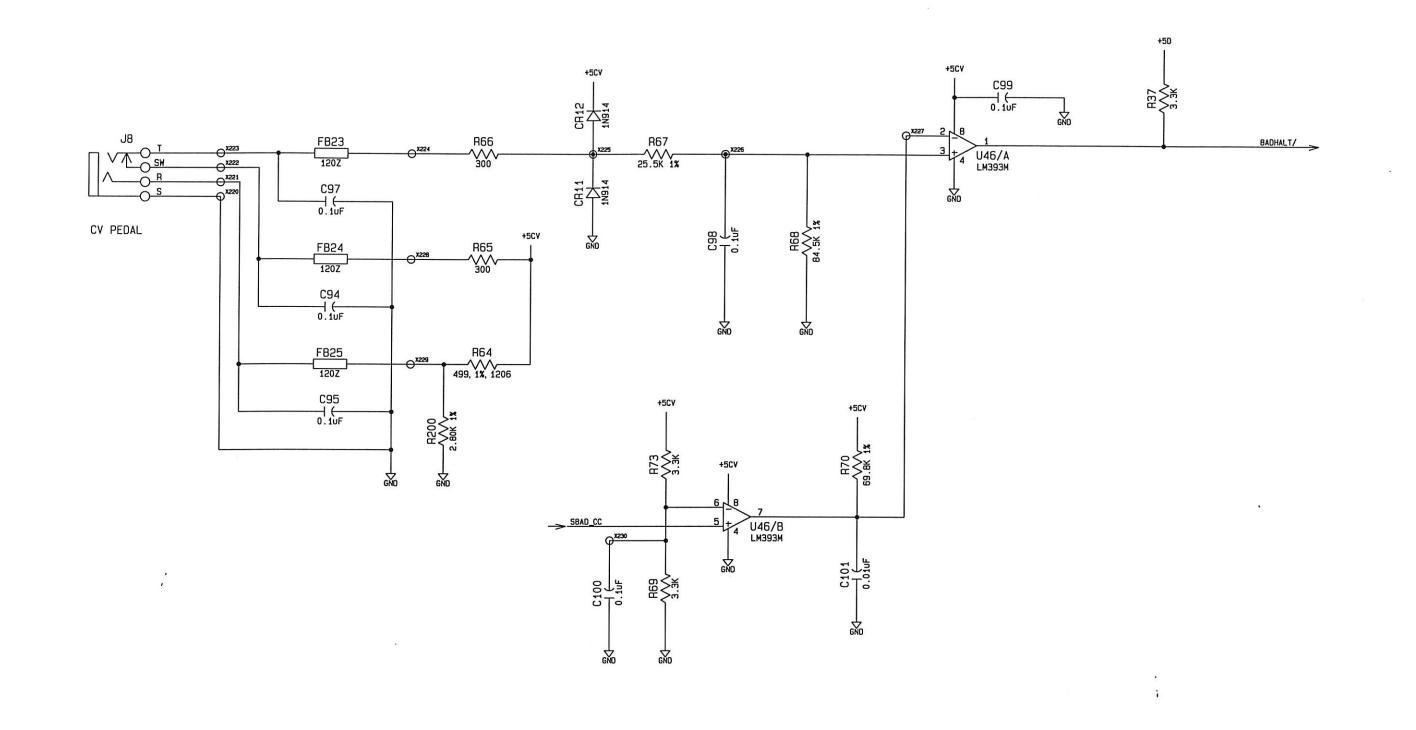


ESP 4

	ENSONIG	Со	rp		
PCB SCHEMATIC					
DP-4+					
DIGITAL BOARD					
SIZE	DWG. NO.	AEV	SHEET	OF	
В	4010024201	С	8	11	







CV PEDAL A/D CONVERTER

	ENSONIG	Co	rp		
PCB SCHEMATIC DP-4+					
DIGITAL BOARD					
SIZE	DWG. NO.	REV	SHEET	0F	
В	4010024201	С	11	11	